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Response Under 37 CFR 1.116  
Expedited Procedure  
Art Unit: 2111

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: T. YAMADA, et al.  
Application No.: 09/603,965  
Filed: June 26, 2000  
For: LOW POWER CONSUMPTION MICROPROCESSOR  
Art Unit: 2111  
Examiner: K. S. Kim

**AMENDMENT AFTER FINAL REJECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

November 5, 2004

Sir:

In response to the Office Action dated August 5, 2004, please amend the above-identified application as listed below and as set forth on the following pages:

**Amendments to the Claims and**

**Remarks are included following the amendments.**